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ESD Design for Analog Circuits is a useful reference for device engineers and circuit designers addressing Analog IC Design problems.

ESD Design for Analog Circuits | Vladislav A. Vashchenko ...

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ESD Analog Circuits and Design is a continuation of the author s series of books on ESD protection. It is an essential reference for ESD, circuit, and semiconductor engineers as well as for layout and design, floorplanning, and ground rule checking and verification developers who are involved in the manufacturing and design process of semiconductor chips.

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ESD Design for Analog Circuits is a useful reference for device engineers and circuit designers addressing Analog IC Design problems. About the Author Dr. Vladislav Vashchenko received MS, Engineer-Physicist (1986) followed by "Ph.D. in Physics of Semiconductors" (1990) from Moscow Institute of Physics and Technology for the study of self-organization phenomena in semiconductor structures ...

ESD Design for Analog Circuits: Vashchenko, Vladislav A ...

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The approach will be to understand ESD protection cells, understand ESD influence on circuit components, apply a co-design approach to combining ESD protection with analog/mixed signal circuits, understand the physical aspects of ICs on ESD, and to avoid common mistakes in ESD protection.

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